TITLE: MAPPING AN ARBITRARY NUMBER OF CONTIGUOUS MEMORY PAGES AT AN ARBITRARY ALIGNMENT
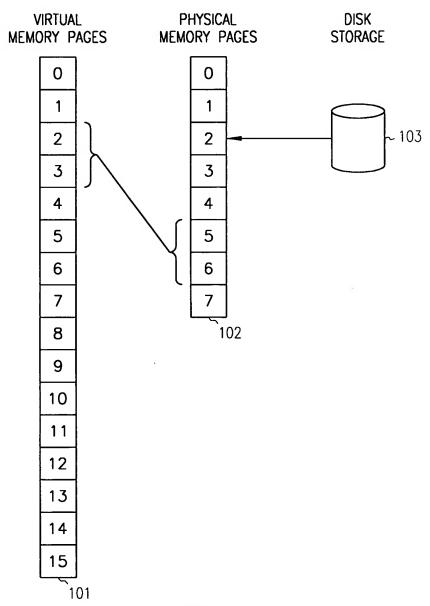INVENTOR NAME: Andrew B. Hastings
SERIAL NO.: 10/643,580

1/2

VIRTUAL
MEMORY PAGES

PHYSICAL
MEMORY PAGES

DISK
STORAGE

0	0	
1	1	
2	2	~103
3	3	
4	4	
5	5	
6	6	
7	7	
8		102
9		
10		
11		
12		
13		
14		
15		

101

FIG. 1

TITLE: MAPPING AN ARBITRARY NUMBER OF CONTIGUOUS MEMORY PAGES AT AN ARBITRARY ALIGNMENT
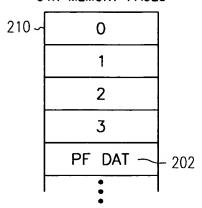INVENTOR NAME: Andrew B. Hostings
SERIAL NO.: 10/643,580

2/2

64K MEMORY PAGES

210	0
	1
	2
	3
	PF DAT — 202
	⋮

FIG. 2

PF DAT

PAGE	0	1	2	3	4	5	6	
FREE BIT	0	0	1	0	0	1	1	•••
⋮								

FIG. 3

64-BIT WORD

0	0	1	0	0	1	1	•••

FIG. 4